

Fig. 1

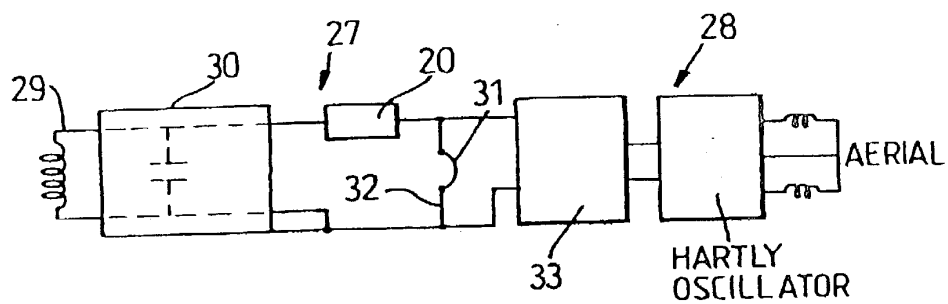


Fig. 2a

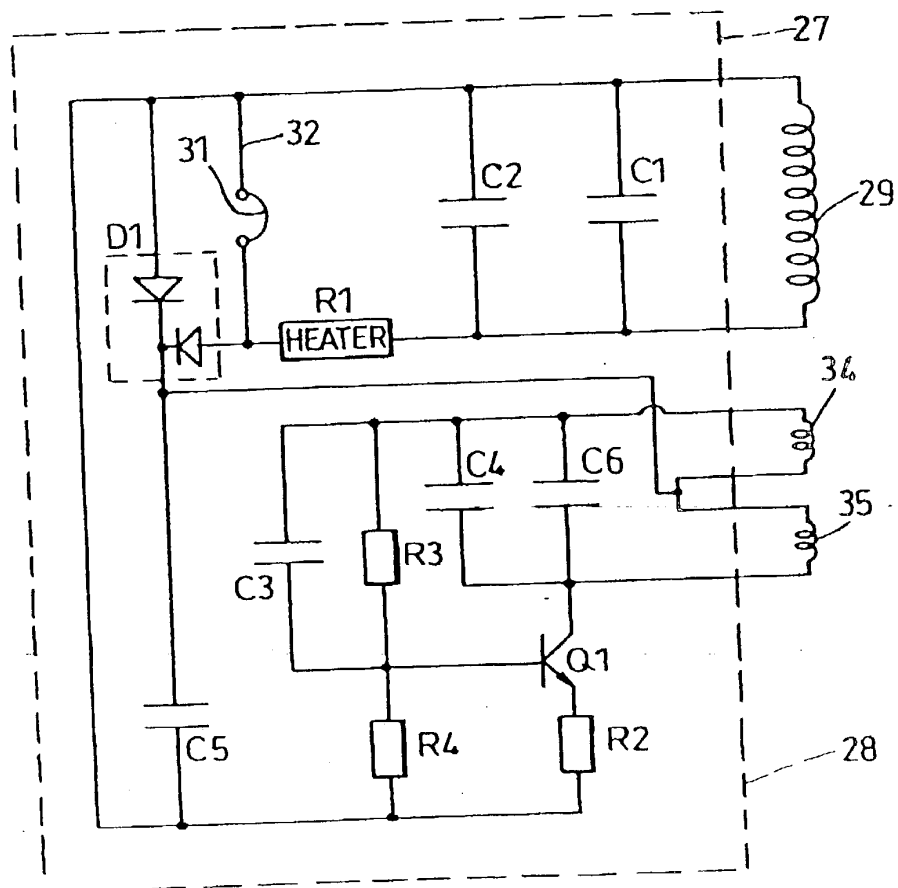


Fig. 2b

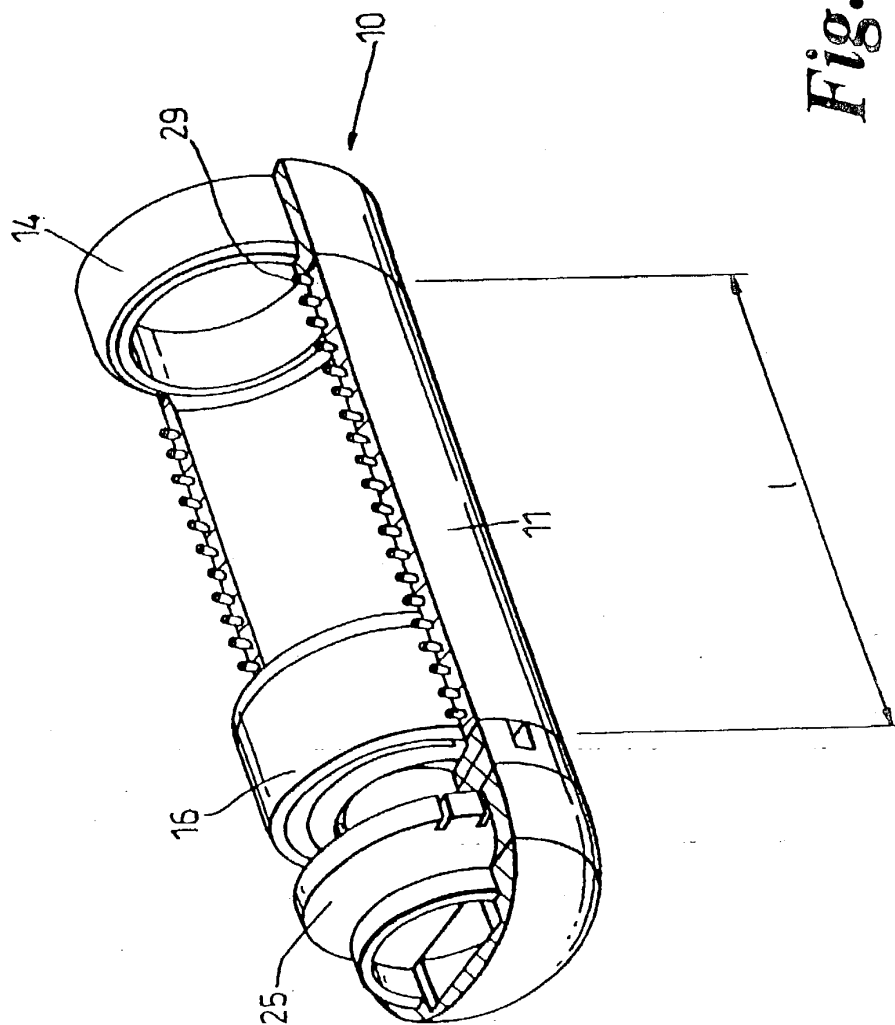


Fig. 3

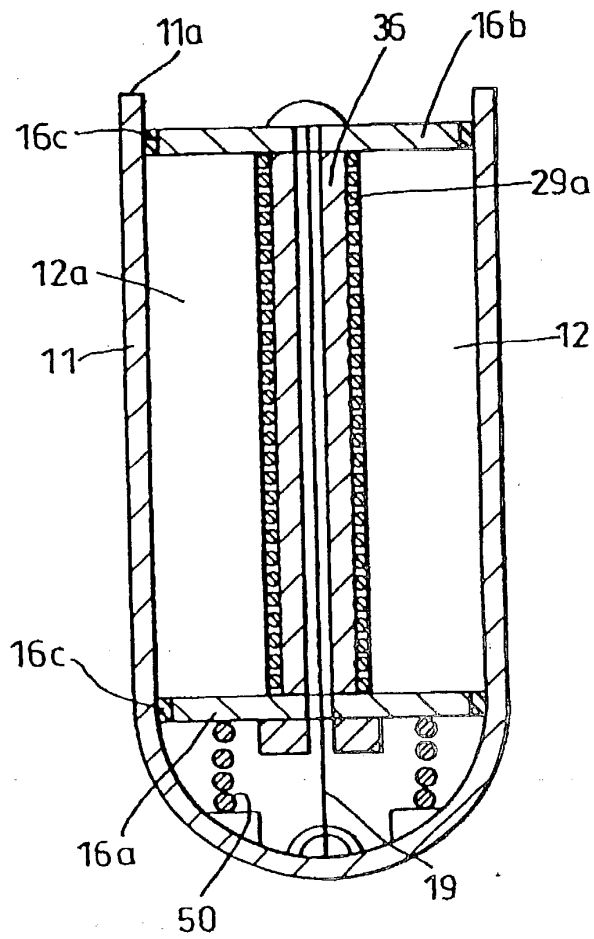


Fig. 4

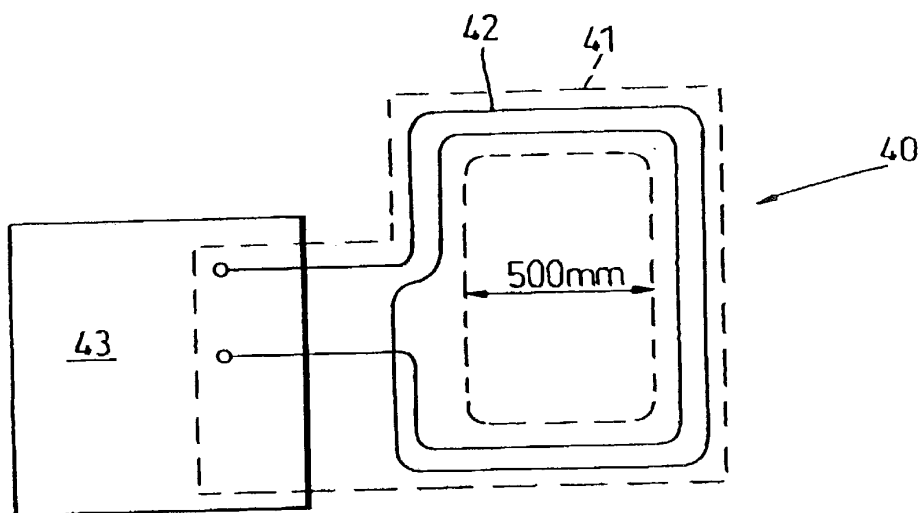


Fig. 5a

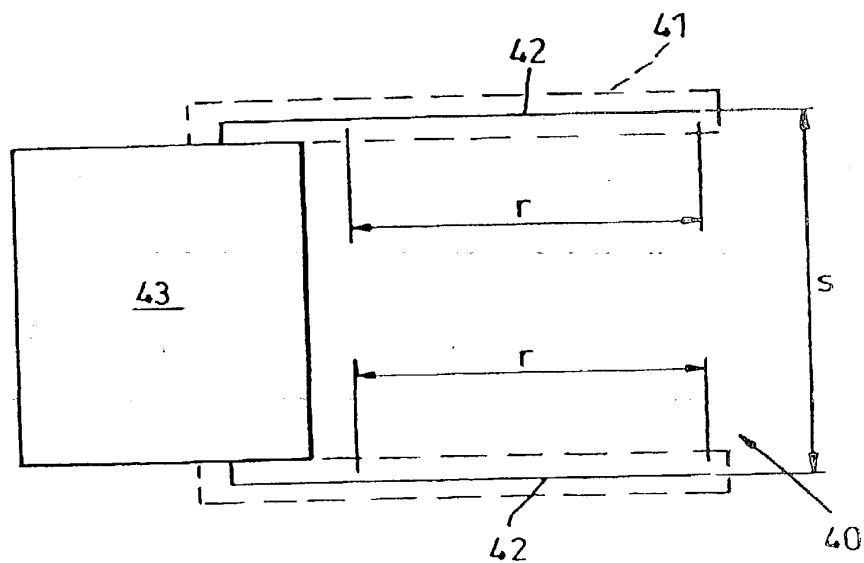


Fig. 5b

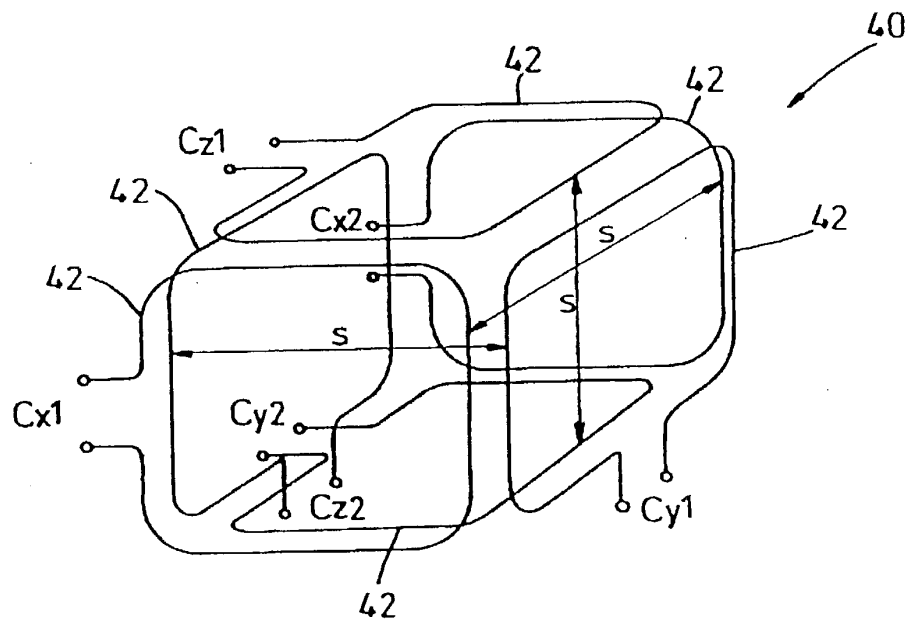


Fig. 6

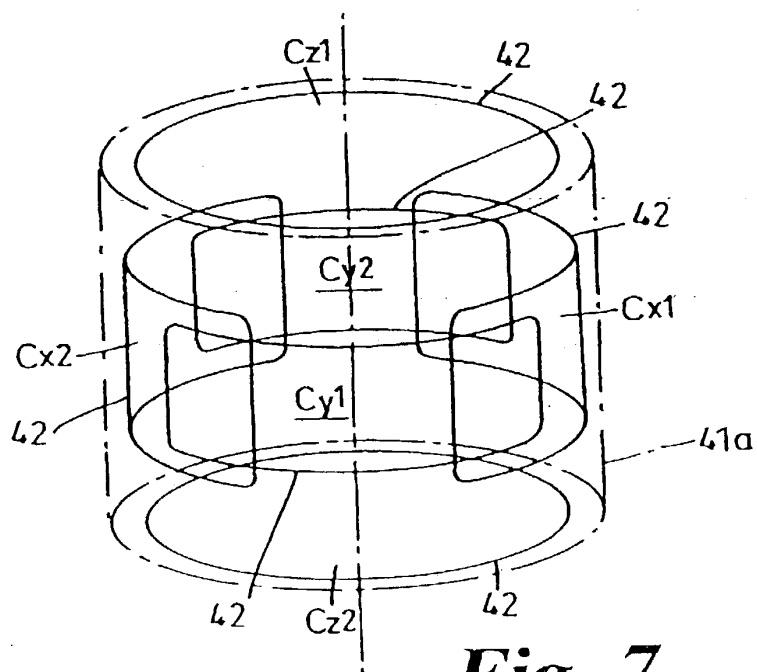


Fig. 7

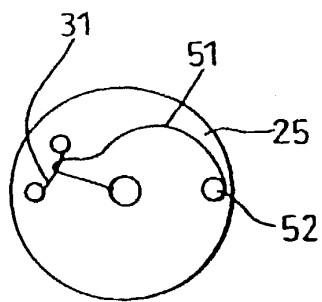


Fig. 8a

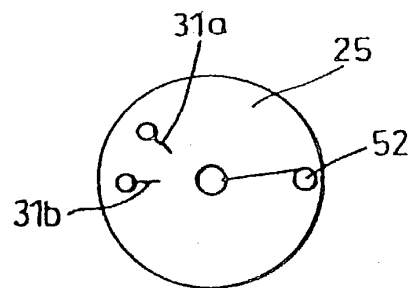
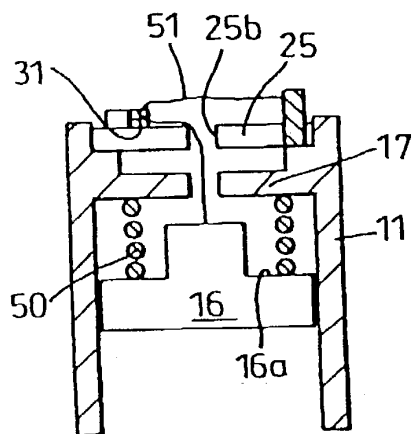
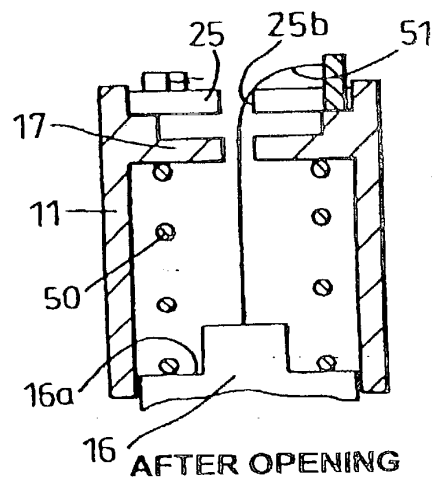


Fig. 9a



BEFORE OPENING

Fig. 8b



AFTER OPENING

Fig. 9b

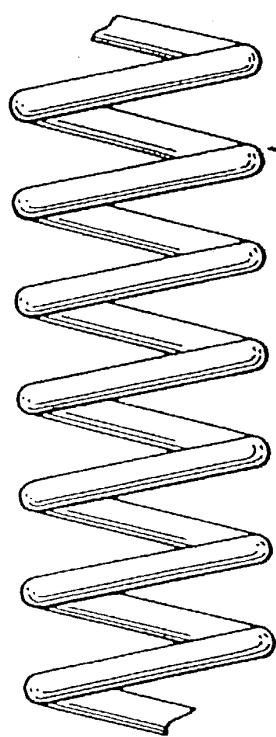


Fig. 10a
(PRIOR ART)

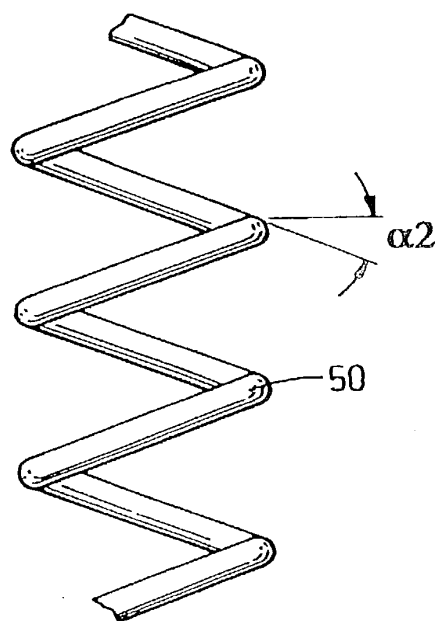


Fig. 10b

$$\alpha_2 > \alpha_1$$

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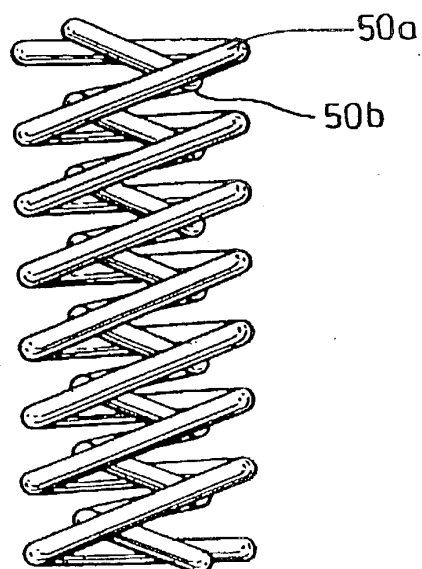


Fig. 11a

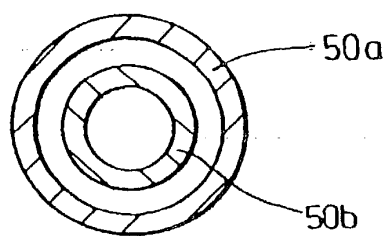


Fig. 11b

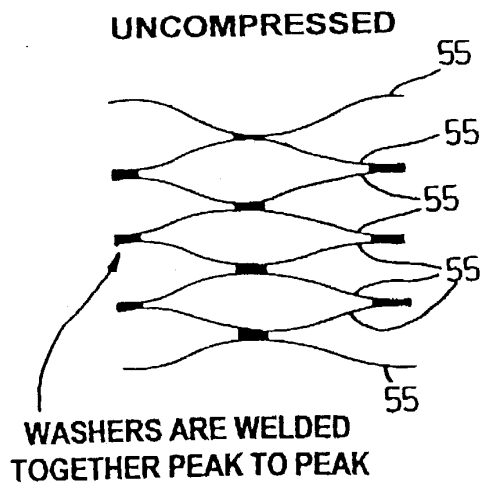


Fig. 12a

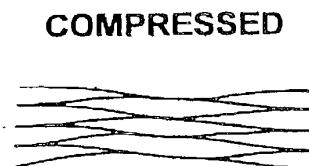


Fig. 12b

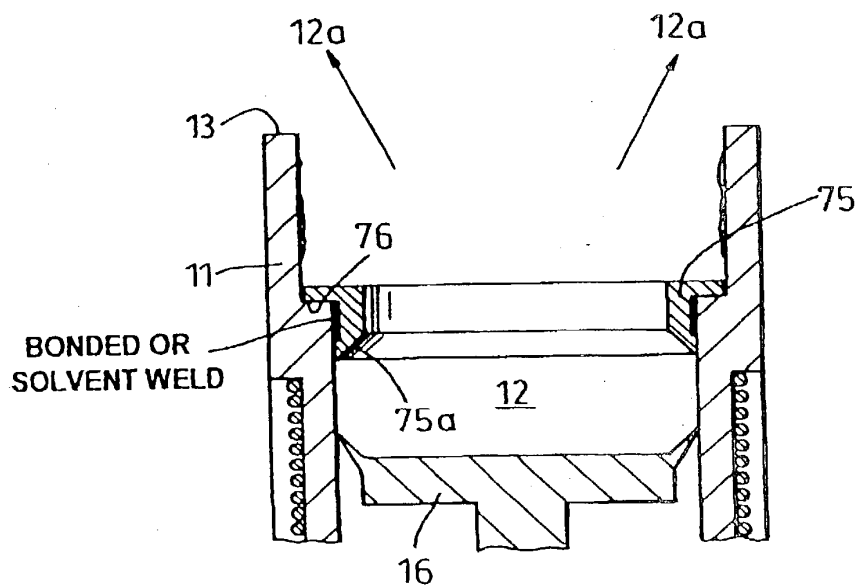


Fig. 13

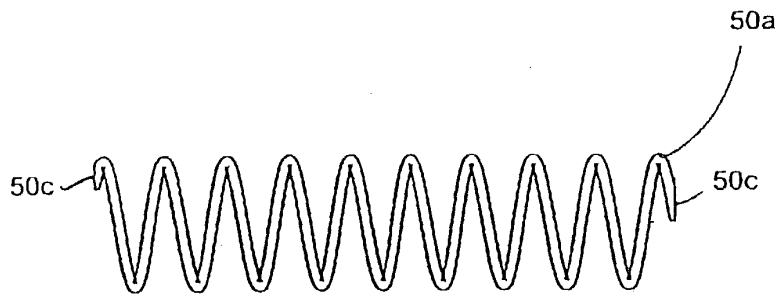


FIGURE 14a

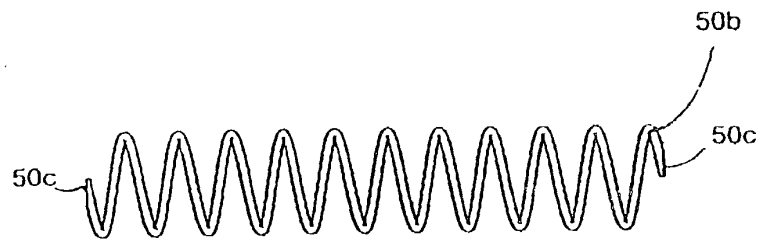


FIGURE 14b

Figure 5.3.1

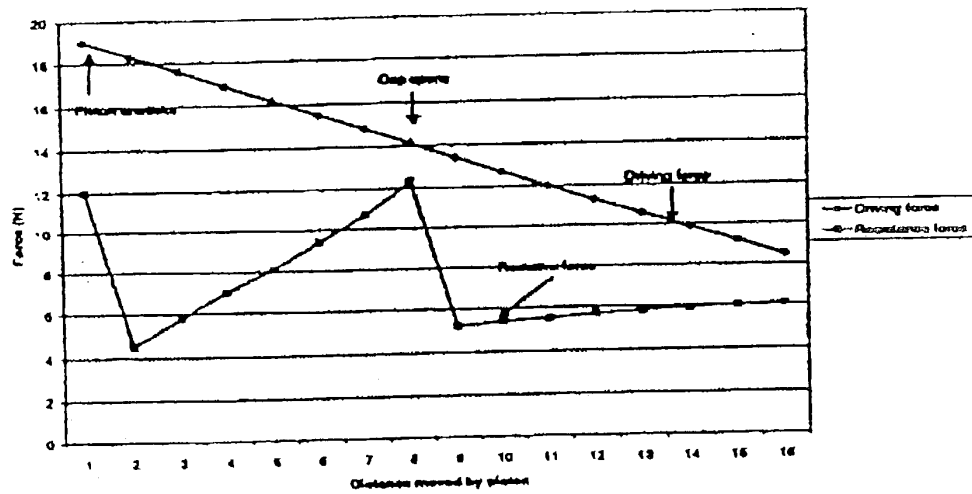
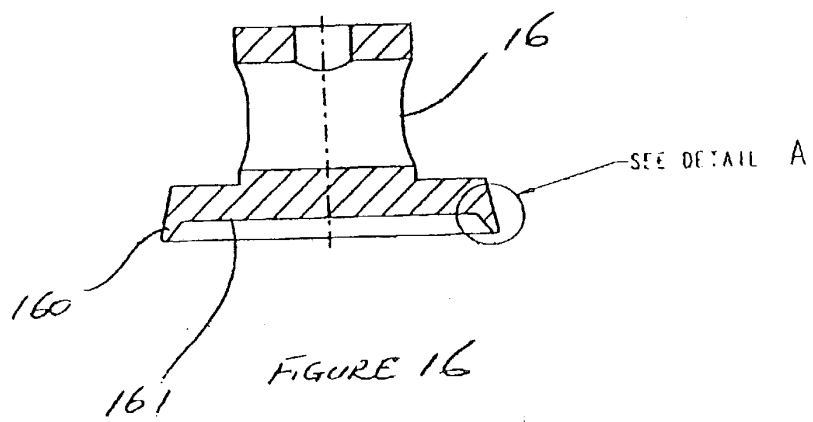


FIGURE 15



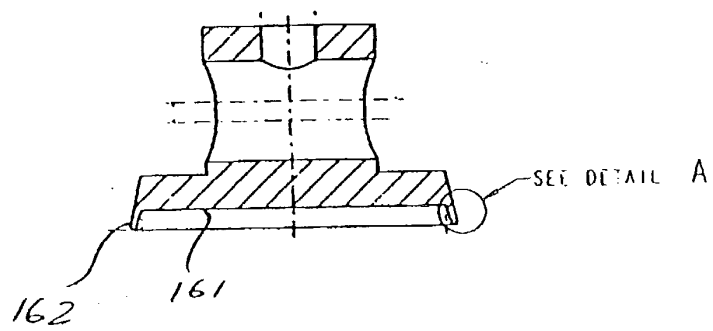
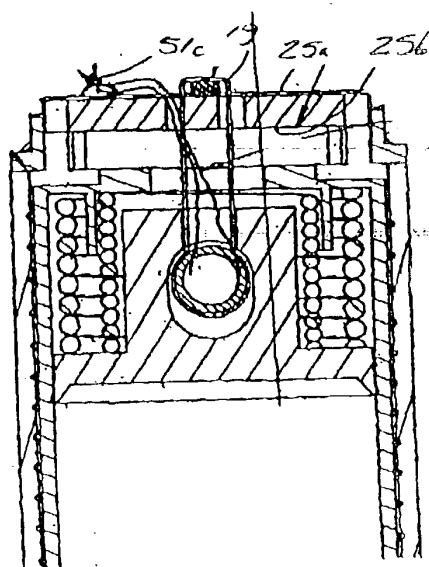
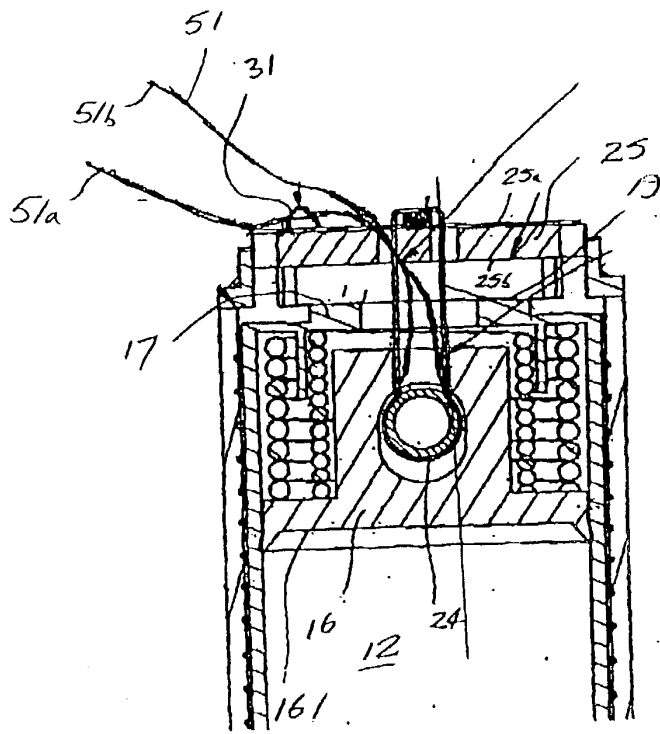


FIGURE 17



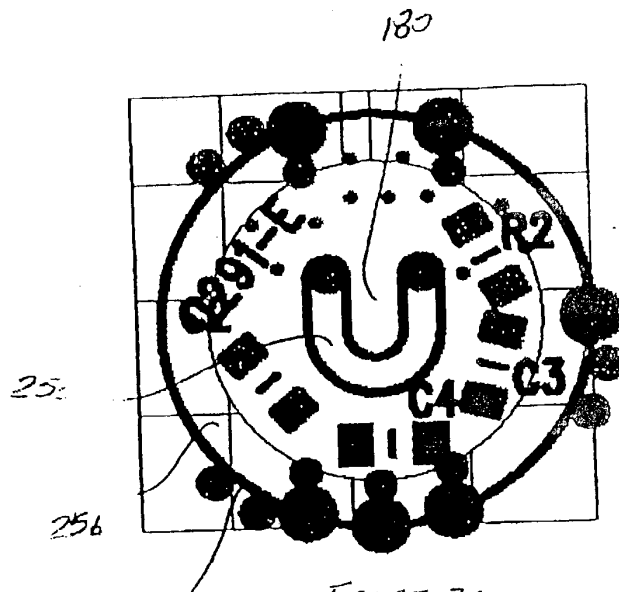


FIGURE 21

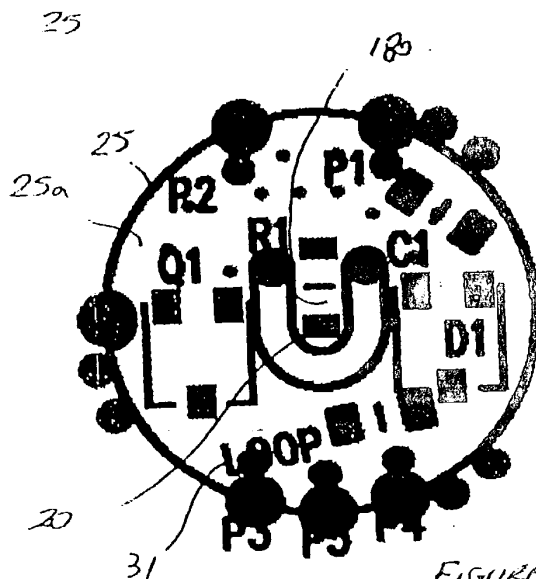


FIGURE 20

New pcb layouts